

AN ABSTRACT OF THE THESIS OF

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(Name of student) (Degree)

in Mathematics presented on December 17, 1969
(Major) (Date)

Title: A TURING MACHINE PROGRAMMING LANGUAGE

Abstract approved: Redacted for Privacy
(Professor Harry Goheen)

This thesis documents a new language which facilitates the construction of Turing machines. The language translator is written in Compass and has been debugged and is available for use on the CDC 3300.

A Turing Machine Programming Language

by

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A THESIS

submitted to

Oregon State University

in partial fulfillment of
the requirements for the
degree of

Master of Arts

June 1970

APPROVED:

Redacted for Privacy

Professor of Mathematics
in charge of major

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Date thesis is presented

December 17, 1969

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ACKNOWLEDGEMENTS

I would like to thank Professor H.E. Goheen for his help and encouragement in the development of this thesis.

I would like to thank Charlene Laski for typing the thesis.

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A TURING MACHINE PROGRAMMING LANGUAGE

I. INTRODUCTION

This thesis develops a symbolic language to facilitate the construction of Turing machines. By now, the importance of Turing machines is well-known. They are recognized as an excellent tool in constructive proof theory, as well as an important source of examples in automata theory. For an introduction to Turing machine theory, see Davis (4), Hermes (5). For important examples of the power of Turing machines see Kleene (6) and Minsky (9).

Existing techniques for constructing Turing machines are inadequate. Construction of symbolic languages for processing automata is an important but neglected area of automatic programming. As Knuth has noted "... most current papers on automata theory have not advanced past the 'machine language' programming stage; authors have been reluctant to simplify their constructions except by using classical mathematical notations which are cumbersome for this purpose.

Symbolic programs are considerably easier to write and to read, and they are less likely to contain careless errors. So it is surprising that these ideas have not

already spread from computer programming to automata programming."¹

The only previous Turing machine simulator language in the literature belongs to M.W. Curtis (3). Curtis's language, written for the IBM 1620, seems to have all the failings noted by Knuth above. It is a language at the machine level and employs mnemonics quite divorced from the structure of Turing machine operations.

The language to be described is a higher level symbolic language, which has been implemented on the CDC 3300. A program processed in this language yields as output a Turing machine in a form usable by the TASP² compiler (if desired) as well as a printout of the resulting quintuples.

To test the processor, Watanabe's Universal Turing machine (12) is encoded in this language, and the resulting program may be compared with the encoding of a universal Turing machine by Curtis, who also used Watanabe's version.

The possibility of encoding a Universal Turing machine gives a proof of the universality of the language in encoding algorithms. This fact is also proved directly in Chapter 3.

1. Knuth, D.E. and R.H. Bigelow. Programming language for Automata. Journal of the Association of Computing Machines. 14:615. 1967.

2. TASP is a Turing Automation Simulation Program.

II. DESCRIPTION

This section consists of an informal description of the programming language, followed by rigorous definition of allowable syntactical structures using the notation of Backus-Naur (2).

In order to describe the language, we shall study it in connection with an example. Consider the problem of devising an algorithm to transform arithmetic expressions into Lukasiewicz Normal Form (8). A Turing machine which will perform such an algorithm is given below:

	H	D	S	(X	Y	Z)	+	-	*	/
1	2RH	1RS	2RH		1RX	1RY	1RZ	1RH	1R+	1R-	1R*	1R/
2	3L)	2RD	!	2R(11LX	11LY	11LZ	3L)	2R+	2R-	2R*	2R/
3	1RS	3LD		9RD					4RD	5RD	6RD	7RD
4	4RH	4RD	8L+	4R(4RX	4RY	4RZ	4R)	4R+	4R-	4R*	4R/
5	5RH	5RD	8L-	5R(5RX	5RY	5RZ	5R)	5R+	5R-	5P*	5R/
6	6RH	6RD	8L*	6R(6RX	6RY	6RZ	6R)	6R+	6R-	6R*	6R/
7	7RH	7RD	8L/	7R(7RX	7RY	7RZ	7R)	7R+	7R-	7R*	7R/
8	8LH	2RD		8R(8RX	8RY	3RZ	8L)	8R+	8L-	8L*	8L/
9		9RD			13RD	14RD	15RD	10RD	9R+	9R-		
10	3L)	2R*		10L(10LX	10LY	10LZ	3L)	2R+	2R-	2R*	2R/
11	9RH	11LD		9R(12L+	12L-	9R*	9R/
12	9RH	12LD		9R(4RD	5RD	6RD	7RD
13	13RH		16LX	13R(13RX	13RY	13RZ	13R)	13R+	13R-	13R*	13R/
14	14RH		16LY	14R(14RX	14RY	14RZ	14R)	14R+	14R-	14R*	14R/
15	15RH		16LZ	15R(15RX	15RY	15RZ	15R)	15R+	15R-	15R*	15R/
16	16LH	10RD		16L(16RX	16LY	16LZ	16L)	16L+	16L-	16L*	16L/

Figure 1

The above assumes the arithmetic expression is originally given in the standard form with delimiter "h" at each end. We also follow the convention that the Turing machine is to commence its operation in State 1, looking at the left delimiter "h", and that the expression involves only the three variables $x, y,$ and z ; the four operations, viz. multiplication, "*", addition, "+", subtraction, "-", and division, "/", and left parenthesis, "(", and right parenthesis, ")".

One may describe the Turing machine of Figure 1 in the following terms. For purposes of description consider the program divided into a number of blocks; first the heading block followed by a number of state blocks and finally the terminator block. In the heading block, the first card is the title card which is read by the processor and then printed at the top of each page of output with the time and date of the run. The next card is the Turing machine card. This card is necessary since more than one Turing machine may be processed in the same run.

The third card of the deck is the alphabet card. This card designates to the processor the set of characters which the Turing machine will recognize as its alphabet. The choice of characters for the Turing machine alphabet is limited to 47 of the 48 Standard Key punch Characters ABC ... XYZ 012...9 + - / * . , = () \$ the 48th character (space or blank) being reserved as a separator.

Next follows the state blocks. Each of these blocks is equivalent to a Turing machine state as described by its set of quintuples in that state.

The last card of the deck is the termination card END. This card denotes the end of compilation. At this point, if no errors have been detected, the quintuples are printed and the TASP processor is called. If any error is detected an appropriate message is printed along with the line number of the place of error detection and compilation is terminated.

A useful feature of this language is that state blocks correspond in a one-to-one fashion with states in the compiled Turing machine. The faithfulness of the number of states demonstrates the usefulness of this language in researches involving minimal state machines.

In terms of the above discussion the Turing machine of Figure 1 is presented in Figure 2.

Figure 2.

A TRANSFORMER OF ARITHMETIC EXPRESSION TO POLISH STRINGS

HEAD BLOCK	}	A LIST IS FORMED TO THE RIGHT OF THE INPUT STRING WHICH WHEN DONE CONTAINS THE POLISH STRING
		TURING MACHINE 1
		ALPHABET H D S : (X Y Z) + - * /

(continued)

STATE BLOCK {

STATE 1 BEGIN AND CLEAN UP AT THE END
SEARCH RIGHT FOR H AND D AND S AND)
H GO RIGHT TO STATE 2
D CHANGE TO S
S CHANGE TO H AND GO RIGHT TO STATE 2
) CHANGE TO H

STATE 2 LOOK FOR THE FIRST X Y Z OR H
SEARCH RIGHT FOR H AND S AND X AND Y AND Z AND)
H CHANGE TO) AND GO LEFT TO STATE 3
S GO TO STOP
X GO LEFT TO 11
Y GO LEFT TO 11
Z GO LEFT TO 11
) GO LEFT TO 3

STATE 3 LOOK FOR FIRST OPERATOR
SEARCH RIGHT FOR H (+ - * /
H CHANGE TO S AND GO R TO 1
(CHANGE TO D AND GO R TO 9
+ CHANGE TO D AND GO R TO 4
- CHANGE TO D AND GO R TO 5
* CHANGE TO D AND GO R TO 6
/ CHANGE TO D AND GO R TO 7

STATE 4 PUT THE + ON THE OUTPUT SIDE
SEARCH RIGHT FOR S
S CHANGE TO + AND GO LEFT TO 8

(continued)

STATE 5 PUT THE - ON THE OUTPUT SIDE
SEARCH RIGHT FOR S
S CHANGE TO - AND GO LEFT TO 8

STATE 6 PUT THE * ON THE OUTPUT SIDE
SEARCH RIGHT FOR S
S CHANGE TO * AND GO LEFT TO 8

STATE 7 PUT THE / ON THE OUTPUT SIDE
SEARCH RIGHT FOR S
S CHANGE TO / AND GO LEFT TO 8

STATE 8 GO BACK TO INPUT STRING
SEARCH LEFT FOR D
D GO RIGHT TO 2

STATE 9 LOOK FOR THE VARIABLES IN INPUT AND
REMOVE THEM
SEARCH RIGHT FOR X Y Z)
X CHANGE TO D AND GO RIGHT TO 13
Y CHANGE TO D AND GO RIGHT TO 14
Z CHANGE TO D AND GO RIGHT TO 15
) CHANGE TO D AND GO RIGHT TO 10

STATE 10 * AND / WRITTEN AFTER PRECEDENCE RULES
EXECUTED
SEARCH LEFT FOR H (+ - * /
H CHANGE TO) AND GO LEFT TO 3
D CHANGE TO * AND GO RIGHT TO 2
+ GO R TO 2
- GO R TO 2

(continued)

```

* GO R TO 2
/ GO R TO 2
STATE 11 FIND FIRST OPERATOR AFTER A VARIABLE
SEARCH LEFT FOR H ( + - * /
H GO R TO 9
( GO R TO 9
+ GO L TO 12
- GO L TO 12
* GO R TO 9
/ GO R TO 9
STATE 12 PRECEDENCE CHECK ON OPERATORS + AND -
WRITTEN
SEARCH LEFT FOR H ( + - * /
H GO R TO 9
( GO R TO 9
+ CHANGE TO D AND GO R TO 4
- CHANGE TO D AND GO R TO 5
* CHANGE TO D AND GO R TO 6
/ CHANGE TO D AND GO R TO 7
STATE 13 PUT THE X ON THE OUTPUT SIDE
SEARCH R FOR S
S CHANGE TO X AND GO LEFT TO 16
STATE 14 PUT THE Y ON THE OUTPUT SIDE
SEARCH R FOR S
S CHANGE TO Y AND GO LEFT TO 16

```

(continued)

STATE 15 PUT THE Z ON OUTPUT SIDE
 SEARCH R FOR S
 S CHANGE TO Z AND GO LEFT TO 16
 STATE 16 GO BACK TO INPUT STRING
 SEARCH L FOR D
 D GO R TO 10
 END

Syntactical Construction

1. Basic Symbols

<external alphabet character> ::= <letter> | <digit> |
 <special character>

1.1 <letter> ::= A | B | C | D | E | F | G | H | I | J | K | L | M | N | O | P |
 Q | R | S | T | U | V | W | X | Y | Z

1.2 <digit> ::= 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |

1.3 <special character> ::= + | - | / | * | . | , | = | () | \$

2. <delimiter> ::= <operator> | <separator> | <bracket> |
 <declarator>

2.1 <operator> ::= search | go | change | changing

2.2 <separator> ::= for | to | and | <blank>

2.3 <bracket> ::= state | Turing machine | end

2.4 <declaration> ::= alphabet

3. <expression> ::= <search expression> | <replacement
 expression> | <transfer expression>

3.1 <search expression> ::= search <direction> for
 <list>

3.1.1 <direction> ::= left | right | R | L |

3.1.2 <list> ::= <external alphabet character> |
 <list><blank><external alphabet
 character>

3.2 <replacement statement> ::= changing
 <external alphabet character> to <external
 alphabet character> | changing rest to
 <external alphabet character>

<replacement statement 2> ::= <external
 alphabet character> change to <external
 alphabet character>

3.3 <transfer expression> ::= go <direction> to
 <label> | go to <label>

3.3.1 <label> ::= state <number> | <number> |
stop
 <number>* ::= <digit> | <number><digit>

*restricted to three digits by our TASP processor

4. Syntax

4.1 <basic statement> ::= <search expression> |
 <search expression><replacement expression 1>

4.2 <statement> ::= <transfer statement> |
 <replacement statement>

4.2.1 <transfer statement> ::= <external
 alphabet character><transfer expression>

4.2.2 <replacement statement> ::= <replacement

expression 2>|<replacement expression 2>
and <transfer expression>|<replacement
 expression 2><blank><transfer expression>

4.3 <machine block> ::= <block head><declaration>
 <state block>|<machine block><state block>|
 <machine block><machine block>

4.3.1 <block head> ::= Turing machine <number>
 (comment)

4.3.2 <declaration> ::= alphabet <list>

4.3.3 <state block> ::= <block label><basic
 statement><statement>|<state block>
 <statement>

4.3.3.1 <block label> ::= state
 <number> (comment)

4.4 <program> ::= <machine block> end

III. IMPLEMENTATION OF THE LANGUAGE

Memory Utilization by the Translator.

The function of the translator is to create a disk file of quintuples for the TASP (11) processor from each input source program. Usually, the source input consists of a card deck of program blocks as described in Chapter 1 preceded by control cards in the format for OS3 (10) whose function it is to load the translator into the machine memory. However, by using other control card options, the source may be put in by other input devices.

The translator uses 73 file blocks of core storage, which is then overlaid, by the TASP processor (11), after the compilation of the quintuple list.

Translator-time Error Detection.

The translator prints out diagnostics after each detection of a source language error. Translation continues however, until the terminator card is reached or until 125 errors have been detected, whichever event occurs first. After detection of the first error, the TASP processor call is suppressed.

Translator Outputs.

The translator output is normally given to the standard system output file, the printer. A file for the

TASP processor is also created. The output for both files consists of the quintuples of the Turing machine which has been compiled. The programmer may request the output on a different device by using other control card options.

Only columns 1 to 72 of each card are interpreted by the translator. The other eight columns are available to the user as a label field, comment field, or sequence field. The Turing machine card, alphabet card, and state card must begin in column 1. All other cards have a free format. At translation time, each card is counted and assigned a line count starting with "1" for reference in error messages.

The format of the source deck is given in Figure 3 and the error diagnostic list in Figure 4.

Figure 3. Source Deck.

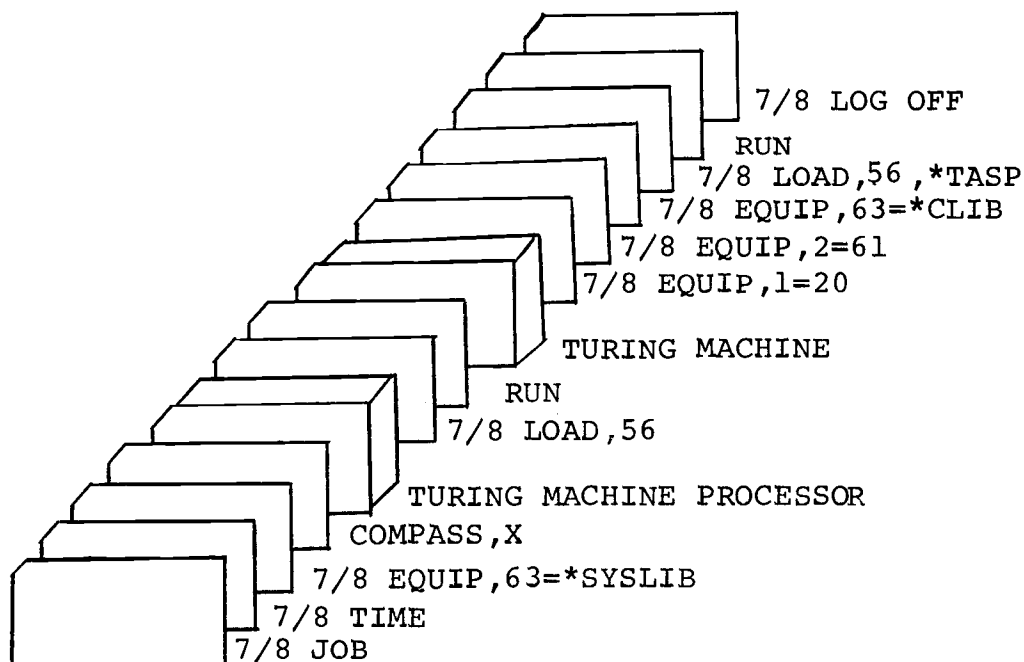


Figure 4. Diagnostic List.

NO TURING CARD

missing a Turing machine card

NO ALPHABET

missing an alphabet card

NO END

missing an end card

TOO MANY ERRORS

more than 125 errors encountered

NO DIRECTION

no direction or an incorrect symbol was found after a search or a go

ILLEGAL STATEMENT AFTER CHANGE

means as stated

SYNTAX ERROR

an illegal statement encountered perhaps a misspelling

UNDEFINED TASK

an illegal expression encountered

NOT IN THE ALPHABET

alphabet character written which is not listed on alphabet card

ERROR IN SYMBOL LIST

a word or two character symbol in symbol list

IV. UNIVERSALITY OF THE TURING MACHINE
PROGRAMMING LANGUAGE
PROCESSOR LANGUAGE

In this section the main assertion stated previously is proved. We show that any Turing machine quintuple may be encoded in this language. This will demonstrate that algorithms which are capable of being encoded as Turing machines may be written in this language, and thus any algorithm may be encoded in this language.

Lemma 1.

Let S be a set of programs which perform algorithms written in language L_1 and let T be a set of programs which perform algorithms written in language L_2 . Let P be a mapping from S to T such that if m is a program of S and the correspondent in T is Pm then

- i) The set of inputs to a program m is the same set as the set of inputs to Pm .
- ii) If i is an input to m then $Pm(i) = m(i)$.

Thus the outputs are the same.

If such a function P does exist then every algorithm encodable in L_1 as an element of S is encodable in L_2 as an element of T .

Proof.

This follows from the definitions. Note that our translator from Turing machine language L_1 to Turing machine L_2 is such a function.

THEOREM 1.

Consider a function $P: S \rightarrow T$ that satisfies the hypotheses of Lemma 1, and suppose there exists a map $Q: T \rightarrow S$ such that PQ is the identity map on S . Then the set of algorithms encodable in L_1 as elements of S equals the set of algorithms encodable in L_2 as elements of T .

Proof.

By Lemma 1 it is sufficient to demonstrate that every algorithm encodable in L_1 by S is also encodable in L_2 by T . To show this, it is sufficient to show that Q also satisfies the hypotheses of the lemma.

Let m be a program in T and i be an input to m . It is necessary to show that $m(i) = Qm(i)$; that is, the output from Qm when i is the input is the same as the output from m .

$$m(i) = (PQm)(i) = [P(Qm)](i) = Qm(i)$$

The last equality holds because P satisfies the hypotheses of the lemma.

QED

THEOREM 2.

Any algorithm may be encoded in the Turing machine processor language.

Proof.

Consider S as the set of all programs which can be written in the Turing machine processor language. Let T be the set of all Turing machines whose alphabet is a subset of the 48 keypunch characters. Basing ourselves on the fact that an algorithm must be performable by some Turing machine which stops, T is the set of all algorithms, since Shannon (11) has shown that the restriction to 48 characters does not restrict the set of algorithms³. Let $P: S \rightarrow T$ be the operation which transforms programs written in the Turing machine language into Turing machines. P is the Turing machine processor. The hypotheses of the lemma are satisfied for P , because the TASP processor acts as a Turing machine simulator; i.e., the operation of a program $a \in S$ is defined to be the operation Pa as a Turing machine.

So by Theorem 1, it is sufficient to find a map $Q: T \rightarrow S$ which acts as a right inverse for P . The remainder of the proof will be the construction of the map Q . Suppose $m \in T$ then $m = (s_1, \dots, s_\alpha)$ where α is the number of states and s_j are the states. Each $s_j = (q_j, \dots, q_{j\beta})$ and the q_{jk} the quintuples of the j^{th} state and β is the total number of symbols. Let

3. Actually reference (11) establishes that two symbols are sufficient.

$\{\gamma_1, \dots, \gamma_\beta\}$ be the set of symbols in the alphabet of m .

Then $q_{jk} = (s_j, \gamma_k, \delta_{jk}, \gamma_{jk}, s_{jk})$ where

s_j is the state in which the Turing machine is in

γ_k is the symbol observed under the Turing head

δ_{jk} is the direction $\in \{\text{RIGHT, LEFT, PLACE}\}$

γ_{jk} is the new state in which the Turing machine goes

s_{jk} is the new symbol to be written on the tape

Let $Q_m = (h, b_1, \dots, b_j, t)$ where $h = \left\{ \begin{array}{l} \text{TURING MACHINE NO. } m \\ \text{ALPHABET } \gamma_1, \dots, \gamma_\beta \end{array} \right\}$

And $b_j = (l_{j1}, \dots, l_{j+2})$ where

$l_{j1} = \text{STATE } j$

$l_{j2} = \text{SEARCH LEFT FOR } \gamma_1, \dots, \gamma_\beta$

For $k > 2$

$l_{jk} = \text{CHANGE TO } \gamma_{jk-2} \text{ AND GO}$

$\delta_{jk-2} \text{ TO } s_{jk-2}$

T = END

h is the heading block

b_j are the state blocks and

t is the termination block.

Clearly, $Q_m \in S$, because Q_m satisfies the rules of syntax explained in section 1.

To conclude this proof, it would be sufficient to show that P applied to Q_m yields m again. But this can be verified by following the Editor flow chart (see Appendix B). The Editor processes each block independently of the

processing of any other block. If one assumes that the Editor is confronted with b_j then it is sufficient to show that the processor outputs $s_j = (q_{j1}, \dots, q_{k\beta})$. The program works in the following manner. Given the input

$b_j = (l_{j1}, \dots, l_{j\beta+2})$ where

$l_{j1} = \text{STATE } j$

$l_{j2} = \text{SEARCH LEFT FOR}$

For $k > 2$

$l_{jk} = \gamma_{k-2} \text{ CHANGE TO } \gamma_{jk-2} \text{ AND GO}$

$\delta_{jk-2} \text{ TO } s_{jk-2}$

it will process each b_j separately.

In the memory of the computer is set up a two dimensional matrix called ALPHABET. This array has four rows: ALPHABET, NEWALPHA, DIRECT, STATE, and β columns. The contents of ALPHABET are $\gamma_1, \dots, \gamma_\beta$. When the program reads a state card l_{j1} it stores j in the memory location called STATENO. This is done in the main program beginning at the location labeled PICKNO. After l_{j1} is processed l_{j2} is read, within the Editor, and then l_{j2} is stored in the 20-word array labeled CARD. The card area is then searched for the word 'SEARCH'. When this is found, the direction following the string 'SEARCH' is put in storage location 'DIRCTION'. Then for $k > 2$, each l_{jk} is successively read into the CARD array. That area is searched for $\gamma_{jk-2}, \delta_{jk-2}, s_{jk-2}$ which are then placed in

the j^{th} columns of NEWALPHA, DIRECT, and STATE respectively. When a b_j has been processed the program jumps to the subroutine called CODEGEN. In this subroutine a check is made to see if every column of NEWALPHA for missing quintuples (an empty column indicates a missing quintuple). Then, if no errors have been detected, representing the j^{th} row, the following is outputted onto the disk file:

Contents of STATENO, contents of the j^{th} column of ALPHABET, contents of the j^{th} column of NEWALPHA, contents of the j^{th} column of DIRECT, contents of the j^{th} column of STATE respectively.

At this time STATENO contains j , the j^{th} column of ALPHABET contains γ_j , the j^{th} column of NEWALPHA contains $\gamma_{j\beta}$, the j^{th} column of DIRECT contains $\delta_{j\beta}$, the j^{th} column of STATE contains $s_{j\beta}$. This is done for each b_j . Thus the output of the processor consists of $(q_{j1}, \dots, q_{j\beta})$ where each $q_{jk} = j, \gamma_j, \delta_{jk}, s_{jk}$.

QED

EPILOGUE

The language as it now stands seems quite satisfactory in clarifying and simplifying the writing of most Turing machines. However, for very long Turing machines there is an addition to the language which would further clarify and reduce the structure of complicated algorithms, a modification which has not as yet been implemented for use on the CDC 3300, namely the patch. The patch would enable one to introduce a subroutine feature into Turing machine programming. The TASP processor does contain a feature whereby a collection of Turing machines may be operated serially, as in the composition of functions. However, this feature, which would allow the use of two short Turing machines in place of one long Turing machine, does not seem to truly reduce the structure of the resulting algorithm. A patch is a method to allow parallel placement of Turing machines instead of sequential placement.

The modification to the description of Chapter 2 necessary for this addition to the language is obtained by replacing paragraph 4.3.3 by the following:

```

<state block> ::= <block label><basic statement>
<statement> | <state block><statement> | <patch block>
<patch statement>

<patch block> ::= <block label> patch <number>
<patch statement> ::= <external alphabet character>

```

is <external alphabet character> | <patch statement>
 <external alphabet character> is <external alphabet
 character>

A typical patch block would be:

STATE j

PATCH n

A_1 is B_1

⋮

A_m is B_m

where j is the state number of the patch state block, n is the Turing machine to be patched to the Turing machine at that stage. $A_1 \dots A_m$ are the external alphabet characters in the original Turing machine which are to play the role of the external alphabet characters $B_1 \dots B_m$ in the Turing machine n . When the processor encounters a patch block, it starts to execute the Turing machine n , substituting characters as described above, until it reaches a STOP command in Turing machine n . At that time it returns to the calling Turing machine at the state block following the patch block.

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APPENDIX 1
Sample Problem
Input and Output

```

TURING MACHINE TRANSLATOR 1219      11/24/69      UNIVERSAL TURING MACHINE
0001  TURING MACHINE 1
0002  ALPHABET 0 1 * A B
0003  STATE 1
0004      SEARCH RIGHT FOR 0 AND 1 AND A AND B
0005      0 CHANGE TO * AND GC LEFT TO 2
0006      1 CHANGE TO * AND GC LEFT TO 3
0007      A CHANGE TO 0 AND GC LEFT TO 5
0008      B CHANGE TO 1
0009  STATE 2
0010      SEARCH LEFT FOR 0 AND 1 AND * AND A AND B
0011      0 CHANGE TO A
0012      1 CHANGE TO B
0013      * CHANGE TO A AND GC L TO 4
0014      A CHANGE TO 0 AND GC R TO 7
0015      B CHANGE TO 1 AND GC R TO 6
0016  STATE 3
0017      SEARCH LEFT FOR 0 AND 1 AND * AND A AND B
0018      0 CHANGE TO A
0019      1 CHANGE TO B
0020      * CHANGE TO A AND GC L TO 2
0021      A GC R TO 7
0022      B GC R TO 8
0023  STATE 4
0024      SEARCH LEFT FOR 0 AND 1 AND * AND A
0025      0 GC R TO STATE 3
0026      1 CHANGE TO B AND GC R TO 5
0027      * GC R TO 1
0028      A CHANGE TO *
0029  STATE 5
0030      SEARCH RIGHT FOR 0 AND 1 AND * AND A
0031      0 GC R TO 4
0032      1 CHANGE TO B AND GC LEFT TO 6
0033      * CHANGE TO A AND GC R TO 5
0034      A CHANGE TO * AND GC L TO 4
0035  STATE 6
0036      SEARCH LEFT FOR 0 1 * A B
0037      0 CHANGE TO A
0038      1 CHANGE TO B
0039      * CHANGE TO 0 AND GC R TO 7
0040      A CHANGE TO 0 AND GC R TO 2
0041      B CHANGE TO 1 AND GC R TO 2
0042  STATE 7
0043      SEARCH RIGHT FOR 0 1 * A B
0044      0 GC R TO 1
0045      1 GC R TO 1
0046      * CHANGE TO 0 AND GC L TO 6
0047      A CHANGE TO 0
0048      B CHANGE TO 1
0049  STATE 8
0050      SEARCH R FOR 0 1 * A B
0051      0 GC L TO 1
0052      1 GC L TO 1
0053      * CHANGE TO 1 AND GC L TO 6
0054      A CHANGE TO 0
0055      B CHANGE TO 1
0056  END

```

TURING MACHINE TRANSLATOR 1219

11/24/69

UNIVERSAL TURING MACHINE

	0	1	*	A	B
001	*L0002	*L0003	*R0001	OL0005	1R0001
002	AL0002	BL0002	AL0004	OR0007	1R0008
003	AL0003	BL0003	AL0002	AR0007	BR0008
004	OR0003	BR0005	*R0001	*L0004	BL0004
005	OR0004	BL0006	AR0005	*L0004	BR0005
006	AL0006	BL0006	OR0007	OR0002	1R0002
007	OR0001	1R0001	OL0006	OR0007	1R0007
008	OL0001	1L0001	1L0006	OR0008	1R0008

010+02*0011+03*001*-01*001A+050001B-011

020+02A0021+02B002*+04A002A-070002B-0A1

030+03A0031+03B003*+02A003A-07A003B-08B

040-0300041-05B004*-01*004A+04*004B+04B

050-0400051+06B005*-05A005A+04*005B-05B

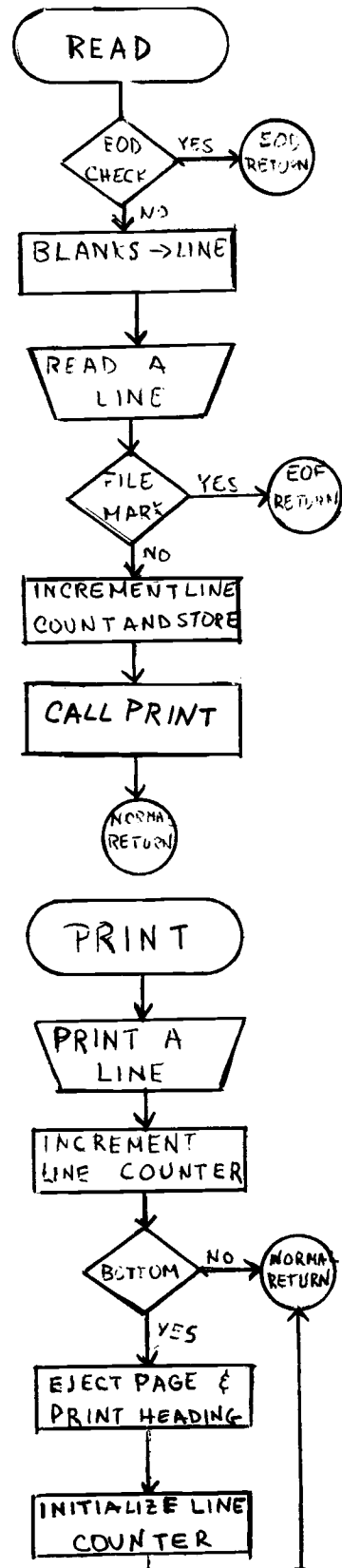
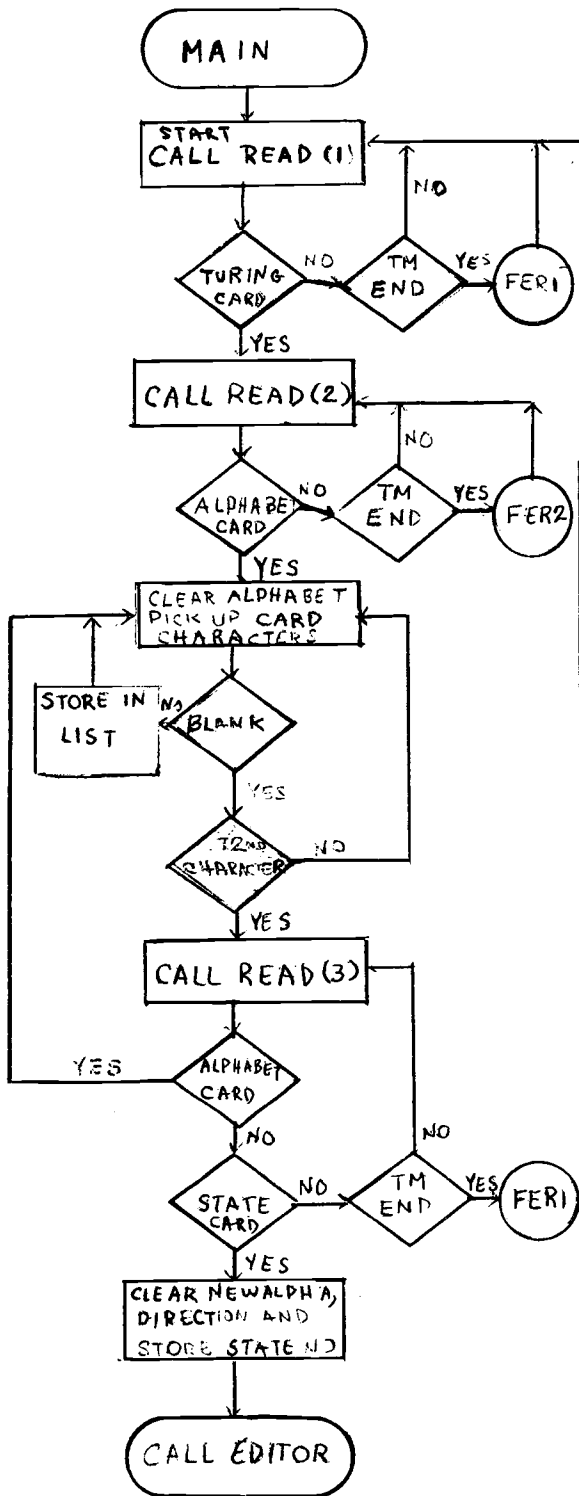
060+06A0061+06B006*-070006A-020006B-021

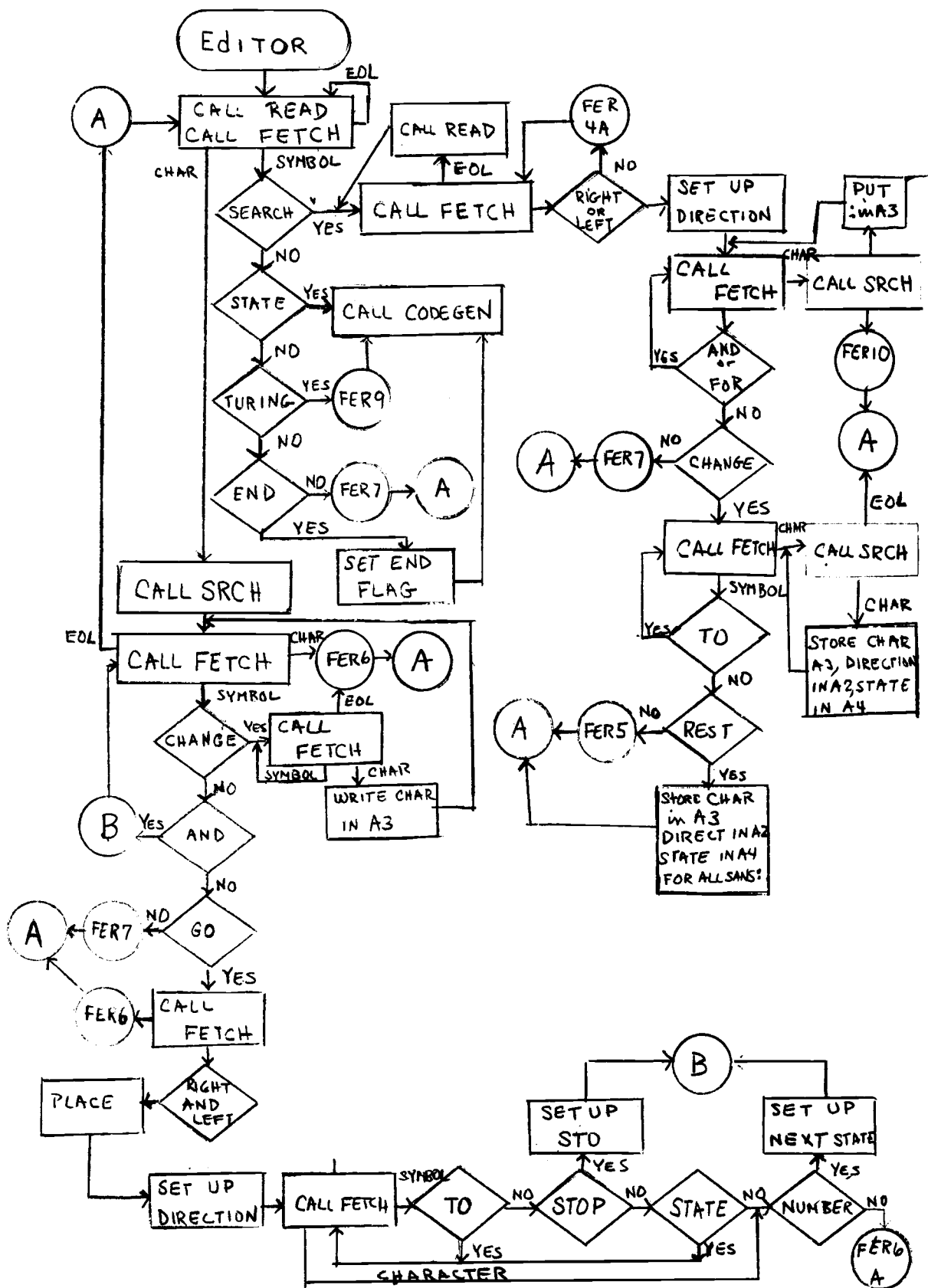
070-0100071-011007*+060007A-070007B-071

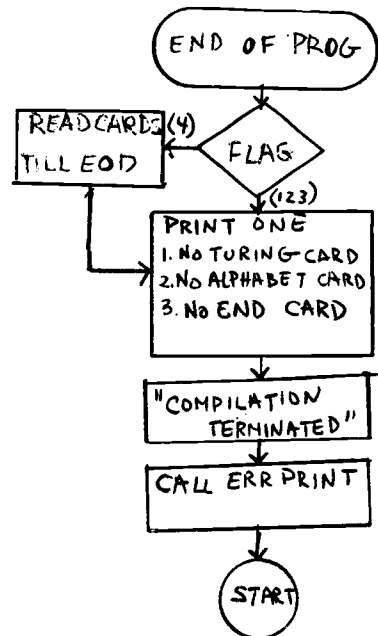
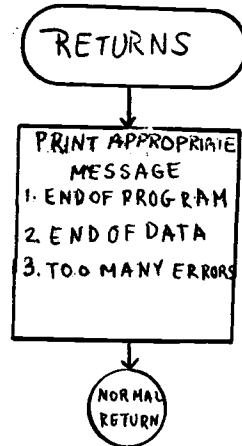
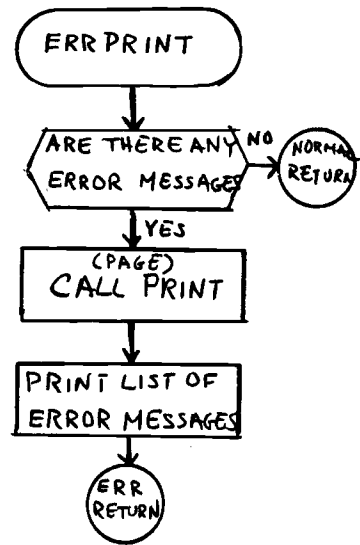
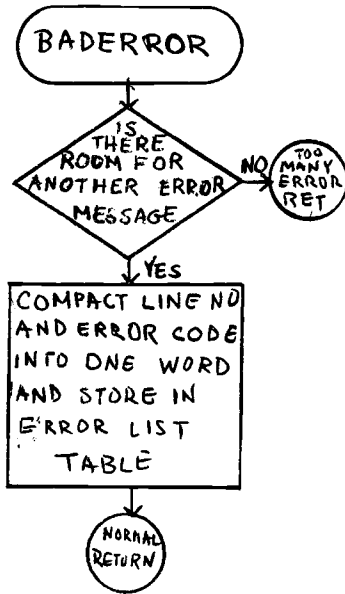
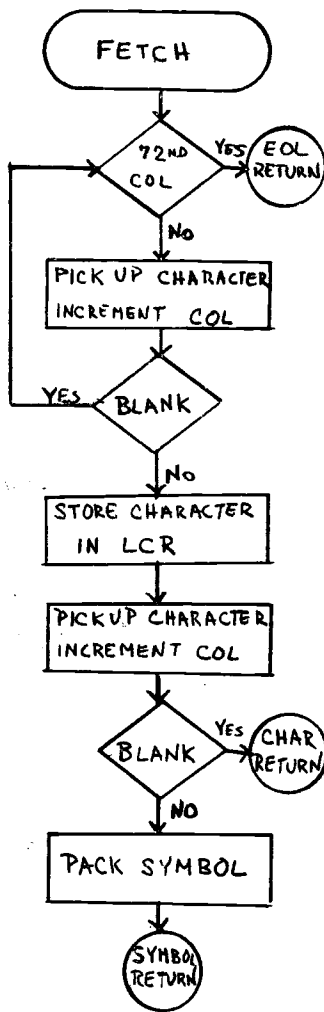
080+0100081+011008*+061008A-080008B-0A1

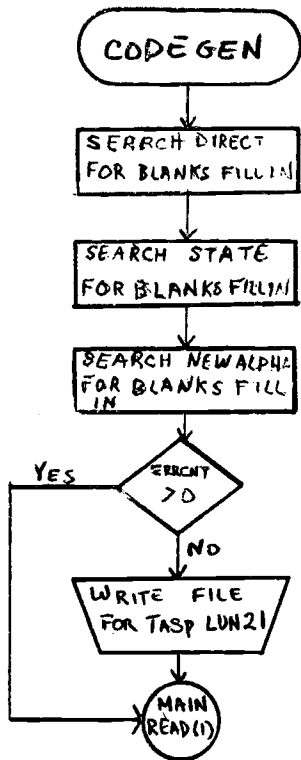
END OF PROGRAM EXECUTION

APPENDIX 2
Flow Charts of
the Turing Machine Processor









APPENDIX 3
Program Listing


```

BLANK   BCD      1,
        DATA
LINE    BSS      23
CARD    EQU      LINE*2
ERRCNT  BSS      1
ERLIST  BSS      128
ALPHABET BSS     192
NEWALPHA EQU     ALPHABET*48
DIRECT  EQU     ALPHABET*96
STATE   EQU     ALPHABET*144
STATENG BSS      1
NALPHA  BSS      1
DIRCTION BSS     1
        END
        IDENT
        READ
        ENTRY   READ, INUNIT
        EXT     STATUS, PRINT, EOD
READ    UJP      **
        ENQ     STATUS
        CNTL, I INUNIT
        SHA     2
        AZJ, LT EOD
        STI     S, XR1, I
        ENI     20, 1
        LDA     BLANK
        STA     CARD, 1
S, XR1  IJD      *-1, 1
        ENI     **, 1
        ENA     CARD
        ENQ     20
INUNIT  READ    **
        SHA     3
        AZJ, LT READ
        LDAQ   LINE
        SHAQ   12
        ADA    #D6666667
        STA    TEMP1
        LPA    #C6D6D6D6
        STA    TEMP2
        SHA    21
        ADA    TEMP2
        XCA, S -D
        ADA    TEMP1
        SHAQ   36
        STAQ   LINE
        LDAQ   CARD*19
        SHAQ   24
        STAQ   CARD*19
        RTJ    PRINT
        LDA    READ
        INA    1
        SWA    READ
        UJP    READ
TEMP1   BSS      1
TEMP2   BSS      1
BLANK   BCD      1,
        DATA
LINE    BSS      23
CARD    EQU      LINE*2
ERRCNT  BSS      1
ERLIST  BSS      128
ALPHABET BSS     192

```

```

NEWALPHA EQU     ALPHABET*48
DIRECT    EQU     ALPHABET*96
STATE     EQU     ALPHABET*144
STATENG   BSS     1
NALPHA    BSS     1
DIRCTION  BSS     1
        END
        IDENT
        EXT     PRINT
        ENTRY   DATE, TIME
        UJP    PRINT, CUTUNIT, PAGE, HEADING, HEDSTR
        ENQ    **
        ENA    23
        ENA    LINE
        WRITE **
        ENA, S -1
        ADA    LINCNT
        STA    LINCNT
        AZJ, BE PRINT
        RTJ    PAGE
        UJP    PRINT
PAGE     UJP    **
        ENA    HEAD
        ENQ    26
        WRITE, I CUTUNIT
        ENA    55
        STA    LINCNT
        UJP    PAGE
LINCNT  BSS     1
HEAD    BCD     12, ITURING MACHINE TRANSLATOR
HEADING BSS     20
HEDSTR  UJP    **
        ECMA   DAT
        RTJ    DATE
        ECMA   TIM
        RTJ    TIME
        UJP    HEDSTR
TIM     EQU     HEAD*7
DAT     EQU, C  HEAD*38
        DATA
LINE    BSS     23
CARD    EQU     LINE*2
ERRCNT  BSS     1
ERLIST  BSS     128
ALPHABET BSS    192
NEWALPHA EQU    ALPHABET*48
DIRECT  EQU    ALPHABET*96
STATE   EQU    ALPHABET*144
STATENG BSS     1
NALPHA  BSS     1
DIRCTION BSS    1
        END
        IDENT
        EXT     RETS
        ENTRY   START, CUTUNIT
        ENA    NORMEND, EOD, ERRABRT
        ENQ    MESS1
        ENA    8
        WRITE **
        ENA, I CUTUNIT
        UJP    START
        ENA    MESS2
        UJP    WRITE
ERRABRT ENA    MESS3
        UJP    WRITE

```


MESS1	BCD	0,-END OF PROGRAM EXECUTION
MESS2	BCD	0,- END OF DATA ENCOUNTERED
MESS3	BCD	0,- TOO MANY ERRORS (ABORT)
	END	
	IDENT	BADERRCR
	EXT	TMERR,NCRUN
	ENTRY	BADERRCR,ERROR
BADERRCR	UJP	00
	STA	TEMP
	ENA,S	-0
	STA	NCRUN
ERRER	STA	FLAG
	STI	S1,1
	LDI	ERRCNT,1
	IS0	129,1
	UJP	OK
	UJP	TMERR
OK	LDAQ	LINE
	SHAQ	-12
	ENA	0
	STI	S2,2
	ENI	3,2
LOOP	SHAQ	3
	SHAQ	3
	IJD	LOOP,2
	SHA	12
	ADA	TEMP
S2	ENI	00,2
	STA	ERLIST,1
	INI	1,1
	STI	ERRCNT,1
S1	ENI	00,1
	LDA	FLAG
	AZJ,LT	BADERRCR
	UJP	ERROR
ERROR	UJP	00
	STA	TEMP
	ENA	0
	STA	FLAG
	UJP	ERRER
FLAG	BSS	1
TEMP	BSS	1
	DATA	
LINE	BSS	23
CARD	EQU	LINE*2
ERRCNT	BSS	1
ERLIST	BSS	128
ALPHABET	BSS	192
NEWALPHA	EQU	ALPHABET*48
DIRECT	EQU	ALPHABET*96
STATE	EQU	ALPHABET*144
STATENC	BSS	
NALPHA	BSS	1
DIRCTION	BSS	1
	END	
	IDENT	FETCH
	ENTRY	FETCH,ITEM,COL,LCR
FETCH	UJP	00
	STI	XR1,1
	STI	XR2,2
	STI	R1,1

	STI	R2,2
	STI	X1,1
	STI	X2,2
COL	ENI	00,1
	UJP	00,2
CON2	INI	1,1
	IS0	72,1
	UJP	CON
	LDA	FETCH
	INA	1
	SWA	FETCH
	ENI	0,1
	STI	COL,1
XR1	ENI	00,1
XR2	ENI	00,2
	UJP	FETCH
CON	LACH	CARD,1
	AGE	608
	UJP	CON1
	UJP	CON2
CON1	SACH	LCR
	INI	1,1
	IS0	72,1
	UJP	CONT
CRET	LDA	FETCH
	INA	2
	SWA	FETCH
	LACH	LCR
	STI	COL,1
X1	ENI	00,1
X2	ENI	00,2
	UJP	FETCH
CONT	LACH	CARD,1
	AGE	608
	UJP	PACK
	UJP	CRET
PACK	LDA	BLANK
	STA	ITEM
	STA	ITEM-I
	ENI	-7,2
	LACH	LCR
PS	SACH	ITEM*7,2
	LACH	CARD,1
	INI	1,1
	IS0	72,1
	UJP	00,2
	UJP	DONE
	AGE	608
DONE	IJI	PS,2
	LDAQ	ITEM
	STI	COL,1
R1	ENI	00,1
R2	ENI	00,2
	UJP	FETCH
ITEM	BSS	2
LCR	BSS	1
BLANK	BCD	1,
	DATA	
LINE	BSS	23
CARD	EQU	LINE*2
ERRCNT	BSS	
ERRLIST	BSS	128

ALPHABET	BSS	192
NEWALPHA	EQU	ALPHABET*48
DIRECT	EQU	ALPHABET*96
STATE	EQU	ALPHABET*144
STATENO	BSS	1
NALPHA	BSS	1
DIRCTION	BSS	1
	END	
	IDENT	EDITOR
	ENTRY	EDITOR,ENDFLAG
	EXT	READ,FETCH,CODEGEN,BADERROR,INTIN
	EXT	NOEND
EDITOR	UJP	*+1
A	RTJ	READ
	UJP	NOEND
	RTJ	FETCH
	UJP	SYM
	UJP	A
	UJP	CHAR
SYM	LDO	SERCHKM
	AQJ,NE	NEXT
FETCH	RTJ	FETCH
	UJP	SYMRL
	UJP	EOL1
	UJP	SYMRL2
EOL1	RTJ	READ
	UJP	NOEND
	UJP	FETCH
SYMRL	LDO	RIGHTMK
	AQJ,NE	LEFT
	ENA	SIB
	STA	DIRCTION
	UJP	NEXT1
LEFT	LDO	LEFTMK
	AQJ,NE	FERR
	ENA	43B
	STA	DIRCTION
	UJP	NEXT1
SYMRL2	LDO	RMASK
	AQJ,NE	LEFT1
	ENA	SIB
	STA	DIRCTION
	UJP	NEXT1
LEFT1	LDO	LMASK
	AQJ,NE	FERR
	ENA	43B
	STA	DIRCTION
	RTJ	FETCH
NEXT1	UJP	SYM2
	UJP	A
	LDI	NALPHA,1
	ENG,S	-0
	RTJ	SRCH
	UJP	FERR
	UJP	NEXT1
SYM2	LDO	ANDMK
	AQJ,EQ	NEXT1
	LDO	FORMK
	AQJ,EQ	NEXT1
	LDO	CHNBMK
	AQJ,NE	FERR
FE	RTJ	FETCH

	UJP	SYN
	UJP	FEN
	RTJ	SRCH
	UJP	FERR
	RTJ	FETCH
	UJP	SYN
	UJP	FEN
	STA	NEWALPHA,1
	LDA	DIRCTION
	STA	DIRECT,1
	LDA	DIRCTION
	LDA	STATENO
	STA	STATE,1
	UJP	FE
FEN	RTJ	READ
	UJP	NOEND
	UJP	FE
SYN	LDO	TOMK
	AQJ,EQ	FE
	LDO	RESTMK
	AQJ,NE	FERR
	RTJ	FETCH
	UJP	SYM
	UJP	A
	STA	NEWLET
	LDI	NALPHA,1
MEQ	LDA	BLANK
	ENG,S	-0
	MEQ	NEWALPHA,1
	UJP	NFOUND
	LDA	DIRCTION
	STA	DIRECT,1
	LDA	STATENO
	STA	STATE,1
	UJP	MEQ
NFOUND	UJP	A
NEXT	LDO	STATENK
	AQJ,EQ	CODEGEN
	LDO	TURIMK
	AQJ,EQ	FB
	LDO	ENDMK
	AQJ,NE	FERR
	ENA,S	-0
	STA	ENDFLAG
	UJP	CODEGEN
FB	ENA	1
	STA	ENDFLAG
	UJP	CODEGEN
CHAR	LDI	NALPHA,1
	ENG,S	-0
	RTJ	SRCH
	UJP	FERR
B	RTJ	FETCH
	UJP	SYMB
	UJP	A
	UJP	FERR
SYMB	LDO	CHNBMK
	AQJ,NE	AN
F	RTJ	FETCH
	UJP	F
	UJP	FERR
	STA	NEWALPHA,1

AN	UJP	B		RTJ	BADERRCR
	LDQ	ANDMK		UJP	A
	AGJ,EQ	B		FER7	ENA
	LDQ	GCMK		RTJ	7
	AGJ,NE	FER7		UJP	BADERRCR
	RTJ	FETCH		ENA	A
	UJP	SKIP		FER9	9
	UJP	FER6		RTJ	BADERRCR
	LDQ	RMASK		UJP	CODEGEN
	AGJ,NE	LEFT2		FER10	10
	ENA	51B		ENA	BADERRCR
	STA	DIRECT,1		RTJ	A
LEFT2	UJP	NEXTA		UJP	**
	LDQ	LMASK		MEQ	ALPHABET,1
	AGJ,NE	FER4		UJP	SRCH
	ENA	43B		LDA	SRCH
	STA	DIRECT,1		INA	1
LEFTS	UJP	NEXTA		SWA	SRCH
	LDQ	LEFTMK		UJP	SRCH
	AGJ,NE	PL		BCD	1,
	ENA	43B		BLANK	BCD
	STA	DIRECT,1		SERCHMK	BCD
	UJP	NEXTA		RIGHTMK	BCD
SKIP	LDQ	RIGHTMK		LEFTMK	BCD
	AGJ,NE	LEFTS		RMASK	BCD
	ENA	51B		LMASK	BCD
	STA	DIRECT,1		ANDMK	BCD
	UJP	NEXTA		FORMK	BCD
	ENA	43B		CHNBGMK	BCD
	STA	DIRECT,1		STC	BCD
PL	UJP	NEXTA		TOMK	BCD
	ENQ	47B		RESTMK	BCD
	STQ	DIRECT,1		STATEMK	BCD
	UJP	SYMB0		TURIMK	BCD
NEXTA	RTJ	FETCH		ENDMK	BCD
	UJP	SYMB0		ENDFLAG	BCD
	UJP	FER6		GCMK	BCD
	UJP	ST2		TEMP	BSS
SYMB0	LDQ	TOMK		NEWLET	BSS
	AGJ,EQ	NEXTA		STOPMK	BCD
	LDQ	STOPMK		DATA	
	AGJ,NE	ST		LINC	BSS
	LDA	STC		CARD	EQU
	STA	STATE,1		ERRCNT	BSS
ST	UJP	B		ERRLIST	BSS
	LDQ	STATEMK		ALPHABET	BSS
	AGJ,EQ	NEXTA		NEWALPHA	EQU
ST2	STA	TEMP		DIRECT	EQU
	LACH	TEMP		STATE	EQU
	ASG	12B		STATENO	BSS
	UJP	SETUP		NALPHA	BSS
	UJP	FER6		DIRCTION	BSS
SETUP	LDA	TEMP		END	
	STA	STATE,1		IDENT	END OF PROGRAM
	UJP	B		EXT	OUTUNIT,ERRPRINT,START,READ
FER4	ENA	4		ENTRY	NOEND,NOEND1,THERR,ECFRET
	RTJ	BADERRCR		RTJ	READ
	UJP	NEXTA		UJP	ECFRET
FER6	ENA	6		UJP	*-2
	RTJ	BADERRCR		ENA	NOEND1
	UJP	A		UJP	4
FER5	ENA	5		NOEND	NOEND1
				NOEND1	3
				STA	EM

```

      ENI      3,1
      LDA      EM
      INA,S    -1
      MUA      =D4
      INA      EMS
      SWA      LOOP
LOOP   LDA      **,1
      STA      ERRM,1
      IJD      LOOP,1
      ENA      ERMS
      ENQ      11
      WRITE,I  CUTUNIT
      ENA      MESS
      ENQ      5
      WRITE,I  CUTUNIT
      RTJ      ERRPRINT
      NCP
      UJP      START
EM     BSS      1
FRMS  BCD      7,0COMPILATION TERMINATED
ERRM  BCD      4,
EMS   BCD      4, NO TURING CARD
      BCD      4, NO ALPHABET
      BCD      4, NO END
      BCD      4, TOO MANY ERRORS
MESS  BCD      5, EXECUTION DELETED
      END
      IDENT   ERRPRINT
      EXT     CUTUNIT,PAGE
      ENTRY   ERRPRINT
FRRPRINT UJP      **
      STI     S1,1
      STI     S2,2
      STI     S3,3
      LDA      ERRCNT
      AZJ,EQ  ERRPRINT
      ENA      HD
      ENQ      4
      WRITE,I  CUTUNIT
      ENA      HD1
      ENQ      1
      WRITE,I  CUTUNIT
      LDA      ERRCNT
      SWA      LOOPEND
      ENI     0,1
LOOP  LDQ      ERLIST,1
      ENA      0
LP    ENI     3,3
      SHA     3
      SHAQ    3
      IJD     LP,3
      STA     ERLINE*2
      LDQ     ERLIST,1
      SHAQ    12
      ENA     0
      SHAQ    12
      INA,S   -1
      MUA     =D9
      INA     M1
      SWA     PLP
      ENI     8,2
PLP   LDA     **,2

```

```

      STA      LCNT,2
      IJD      PLP,2
      ENA      ERLINE
      ENQ      13
      WRITE,I  CUTUNIT
      INI     1,1
LOOPEND I56    **,1
      UJP     LOOP
      ENA     HD1
      ENQ     1
      WRITE,I  CUTUNIT
S1    ENI     **,1
S2    ENI     **,2
S3    ENI     **,3
      LDA     ERRPRINT
      INA     1
      SWA     ERRPRINT
      UJP     ERRPRINT
      HD     BCD      4,0ERROR MESSAGES
      HD1    BCD      1,
      ERLINE BCD      4, LINE
      LCNT   BCD      9,
      M1     BCD      9,
      M2     BCD      9,
      M3     BCD      9,
      M4     BCD      9, NO DIRECTION
      M5     BCD      9, ILLEGAL STATEMENT AFTER CHANGE
      M6     BCD      9, SYNTAX ERROR
      M7     BCD      9, UNDEFINED TASK
      M8     BCD      9,
      M9     BCD      9, NOT IN THE ALPHABET
      M10    BCD      9, ERROR IN SYMBOL LIST
      DATA
      LINE   BSS      23
      CARD   EQU      LINE*2
      ERRCNT BSS      1
      ERLIST BSS      128
      ALPHABET BSS    192
      NEWALPHA EQU     ALPHABET*48
      DIRECT  EQU     ALPHABET*96
      STATE  EQU     ALPHABET*144
      STATENC BSS     1
      NALPHA BSS     1
      DIRICION BSS    1
      END
      IDENT   CODEGEN
      ENTRY   CODEGEN
      EXT     READ3,READ1,ENDFLAG,NORMEND,CUTUNIT,REWIND
      EXT     ERRPRINT
CODEGEN NCP
      LDI     NALPHA,1
MEQ    LDA     BLANK
      ENQ,S   =0
      MEQ     DIRECT,1
      UJP     NFOUND
      LDA     DIRECTION
      STA     DIRECT,1
      UJP     MEQ
NFOUND LDI     NALPHA,1
MEQ2   ENQ,S   =0
      LDA     BLANK
      MEQ     STATE,1

```

	UJP	NFCUND2		ADA		CNE
	LDA	STATENC		STA		NUMB
	STA	STATE,1		LDQ		STATENC
NFCUND2	UJP	MEQ2		AQJ,EQ		BEG
MEQ3	LDI	NALPHA,1		AQJ,BE		NEXT
	ENQ,S	-0		UJP		BEG
	LDA	BLANK		LDI		PLACE,1
	MEQ	NEWALPHA,1	WALPHA	ENI		8,2
	UJP	DCNE	RCT	ENI		16,3
	LDA	ALPHABET,1		LACH		ALPHABET,1
	STA	NEWALPHA,1		SACH		LINE,2
	UJP	MEQ3		INI		4,1
DCNE	ENQ	192		INI		9,2
	ENA	ALPHABET		IJD		RCTS,3
	WRITE	SCRATCH		ENQ		33
	LDA	ENDFLAG		ENA		LINE
	ENQ,S	-0		WRITE,I		CUTUNIT
	AQJ,EQ	WRCTE		RTJ		BLNK
	ENQ	1		LDA		NUMB
	AQJ,EQ	READ1A		INA		1
	UJP	READ3A		STA		NUMB
WRCTE	ENQ	REWIND		UJP		BEGA
	CNTL	SCRATCH		LDA		NALPHA
	RTJ	ERRPRINT		SRA		FORTEEN
	UJP	*+2	NEXT	AQJ,LT		NEW
	UJP	NORMEND		ENQ		1
	ENA	0		LDA		PLACE
	STA	NUMB		INA		14
REG	ENQ	192		STA		PLACE
	ENA	ALPHABET		UJP		WRCTE
	READ	SCRATCH		UJP		**
	RTJ	BLNK	BLNK	LDA		BLANK
BEGA	LDA	NUMB		EWI		33,1
	ENQ	0		STA		LINE,1
	AQJ,EQ	WALPHA		IJD		CLEAR,1
	STA	LINE		UJP		BLNK
	LDI	PLACE,1		UJP		**
	ENI	6,2	ZERO	LDA		ZER
	ENI	16,3		ENI		33,1
RCTA	LACH	NEWALPHA,1		STA		LINE,1
	SACH	LINE,2		IJD		*-1,1
	INI	1,2		UJP		ZERO
	LACH	DIRECT,1		ENQ		REWIND
	SACH	LINE,2		CNTL		SCRATCH
	ENA	4		ENA		0
	STA	TEMP		STA		NUMB
	INI	-4,1		ENI		0,2
DOA	INI	1,2		LDA		NUMB
	INI	1,1		ADA		CNE
	LACH	STATE,1		STA		NUMB
	SACH	LINE,2		LDQ		STATENC
	LDA	TEMP		AQJ,EQ		*+2
	SRA	CNE		AQJ,BE		DID
	STA	TEMP		ENQ		192
	AZJ,NE	DOA		ENA		ALPHABET
	INI	4,2		READ		SCRATCH
	INI	4,1		RTJ		ZER
	IJD	RCTA,3		LDI		NALPHA,3
	ENQ	33		ENI		0,1
	ENA	LINE		LDA		NUMB
	WRITE,I	CUTUNIT		RTJ		VALQ
	LDA	NUMB		STA		BEAT

RET	LDA	BEAT	OCT	100000	
	STA	LINE,2	OCT	1	
	LDA	ALPHABET,1	OCT	12	
	RAD	LINE,2	OCT	144	
	INI	1,1	OCT	1750	
	INI	2,2	BSS	1	
	IJD	BET,3	BCD	1,000R	
	ENI	0,1	BCD	1,000L	
	ENI	1,2	OCT	0	
	LDI	NALPHA,3	RCODE	OCT	40000000
RES	LDA	DIRECT,1	LCODE	OCT	20000000
	LDQ	RMASK	SCRATCH2	EQU	21
	AQJ,EQ	R	ZER	BCD	1,0000
	LDQ	LMASK	SCRATCH	EQU	20
	AQJ,EQ	L	PLACE	BCD	1,0003
	LDA	PCODE	TEMP	BSS	1
	RAD	LINE,2	ONE	BCD	1,0001
	UJP	Q	NUMB	BSS	1
L	LDA	LCODE	FORTEEN	BCD	1,0014
	RAD	LINE,2	BLANK	BCD	1,
	UJP	Q	DATA		
R	LDA	RCODE	LINE	BSS	23
	RAD	LINE,2	CARD	EQU	LINE,2
Q	LDA	NEWALPHA,1	ERRCNT	BSS	1
	RAD	LINE,2	ERRLIST	BSS	120
	LDA	STATE,1	ALPHABET	BSS	192
	RTJ	VALQ	NEWALPHA	EQU	ALPHABET+40
	RAD	LINE,2	DIRECT	EQU	ALPHABET+96
	INI	1,1	STATE	EQU	ALPHABET+144
	INI	2,2	STATENO	BSS	1
	IJD	BES,3	NALPHA	BSS	1
	ENA	LINE	DIRECTION	BSS	1
	LDQ	NALPHA			
	SHQ	1			
	WRITE	61			
	WRITE	SCRATCH2			
	UJP	BETA			
DID	UJP	NORMEND			
VALQ	UJP	**			
	RTJ	BCD			
	ENQ	0			
	SHAQ	6			
	UJP	VALQ			
BCD	UJP	**			
	STI	S1,1			
	ENQ	0			
	ENI	5,1			
BCD1	SBA	BK-1,1			
	AZJ,LT	BCD2			
	SHAQ	24			
	ADA	BK+4,1			
	SHAQ	24			
	UJP	BCD1			
BCD2	ADA	BK-1,1			
	IJD	BCD1,1			
	SHAQ	24			
S1	ENI	** ,1			
	UJP	BCD			
BK	OCT	1			
	OCT	100			
	OCT	10000			
	OCT	100000			